

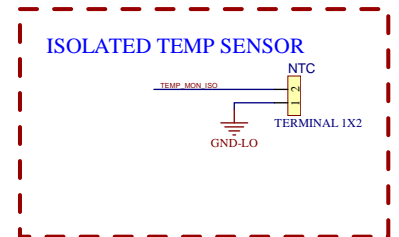
Note:-



FOR MOUNTING OF AGILE CORE:  
J2 IS 14 PIN FEMALE CONNECTOR  
J5 IS 8 PIN FEMALE CONNECTOR  
J6 IS 8 PIN FEMALE CONNECTOR

FOR DIFFERENTIAL INPUTS:  
INSTALL RD1,RD2,RD3,RD4,RD5,RD6  
REMOVE RS1,RS2,RS3,RS4,RS5,RS6

FOR SINGLE ENDED 15V OPERATON:  
INSTALL RS1,RS2,RS3,RS4,RS5,RS6  
REMOVE RD1,RD2,RD3,RD4,RD5,RD6

FOR 5V SINGLE ENDED (NOT RECOMMENDED)  
INSTALL RS1,RS2,RS3,RS4,RS5,RS6  
REMOVE RD1,RD2,RD3,RD4,RD5,RD6  
CHANGE R7, R10 FROM 2K TO 100 OHM



Drawn By: Vipin G		 <b>MICROCHIP</b>	
Engineer: Nitesh S / Vipin G			
PartNumber: <b>SP6CA3</b>	Project Title	Variant: <b>SP6CA3.0</b>	<div>Designed with</div>  <div><b>Altium</b></div> <div><a href="http://www.altium.com">Altium.com</a></div>
<b>SP6 1700V SiC Module Adapter Board</b>			
Sheet Title <b>Control I/O</b>			
Size <b>A3</b>	SCH #: <b>03-SP6CA3</b> PCB #: <b>04-AS-17920</b>	Rev:00 Rev:00	
File: <b>D01_Control I.O.SchDoc</b>		Date: <b>25-Aug-20</b>	Sheet 1 of 2

